/\*

Copyright (c) 2015 Arduino LLC. All right reserved.

This library is free software; you can redistribute it and/or

modify it under the terms of the GNU Lesser General Public

License as published by the Free Software Foundation; either

version 2.1 of the License, or (at your option) any later version.

This library is distributed in the hope that it will be useful,

but WITHOUT ANY WARRANTY; without even the implied warranty of

MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the GNU

Lesser General Public License for more details.

You should have received a copy of the GNU Lesser General Public

License along with this library; if not, write to the Free Software

Foundation, Inc., 51 Franklin St, Fifth Floor, Boston, MA 02110-1301 USA

\*/

/\*

\* Defines for 16 bit timers used with Servo library

\*

\* If \_useTimerX is defined then TimerX is a 16 bit timer on the current board

\* timer16\_Sequence\_t enumerates the sequence that the timers should be allocated

\* \_Nbr\_16timers indicates how many 16 bit timers are available.

\*/

#ifndef \_\_SERVO\_TIMERS\_H\_\_

#define \_\_SERVO\_TIMERS\_H\_\_

/\*\*

\* SAMD Only definitions

\* ---------------------

\*/

// For SAMD:

#define \_useTimer1

//#define \_useTimer2 // <- TODO do not activate until the code in Servo.cpp has been changed in order

// to manage more than one channel per timer on the SAMD architecture

#if defined (\_useTimer1)

#define TC\_FOR\_TIMER1 TC4

#define CHANNEL\_FOR\_TIMER1 0

#define INTENSET\_BIT\_FOR\_TIMER\_1 TC\_INTENSET\_MC0

#define INTENCLR\_BIT\_FOR\_TIMER\_1 TC\_INTENCLR\_MC0

#define INTFLAG\_BIT\_FOR\_TIMER\_1 TC\_INTFLAG\_MC0

#define ID\_TC\_FOR\_TIMER1 ID\_TC4

#define IRQn\_FOR\_TIMER1 TC4\_IRQn

#define HANDLER\_FOR\_TIMER1 TC4\_Handler

#define GCM\_FOR\_TIMER\_1 GCM\_TC4\_TC5

#endif

#if defined (\_useTimer2)

#define TC\_FOR\_TIMER2 TC4

#define CHANNEL\_FOR\_TIMER2 1

#define INTENSET\_BIT\_FOR\_TIMER\_2 TC\_INTENSET\_MC1

#define INTENCLR\_BIT\_FOR\_TIMER\_2 TC\_INTENCLR\_MC1

#define ID\_TC\_FOR\_TIMER2 ID\_TC4

#define IRQn\_FOR\_TIMER2 TC4\_IRQn

#define HANDLER\_FOR\_TIMER2 TC4\_Handler

#define GCM\_FOR\_TIMER\_2 GCM\_TC4\_TC5

#endif

typedef enum {

#if defined (\_useTimer1)

\_timer1,

#endif

#if defined (\_useTimer2)

\_timer2,

#endif

\_Nbr\_16timers } timer16\_Sequence\_t;

#endif // \_\_SERVO\_TIMERS\_H\_\_